

Fig. 1
(PRIOR ART)

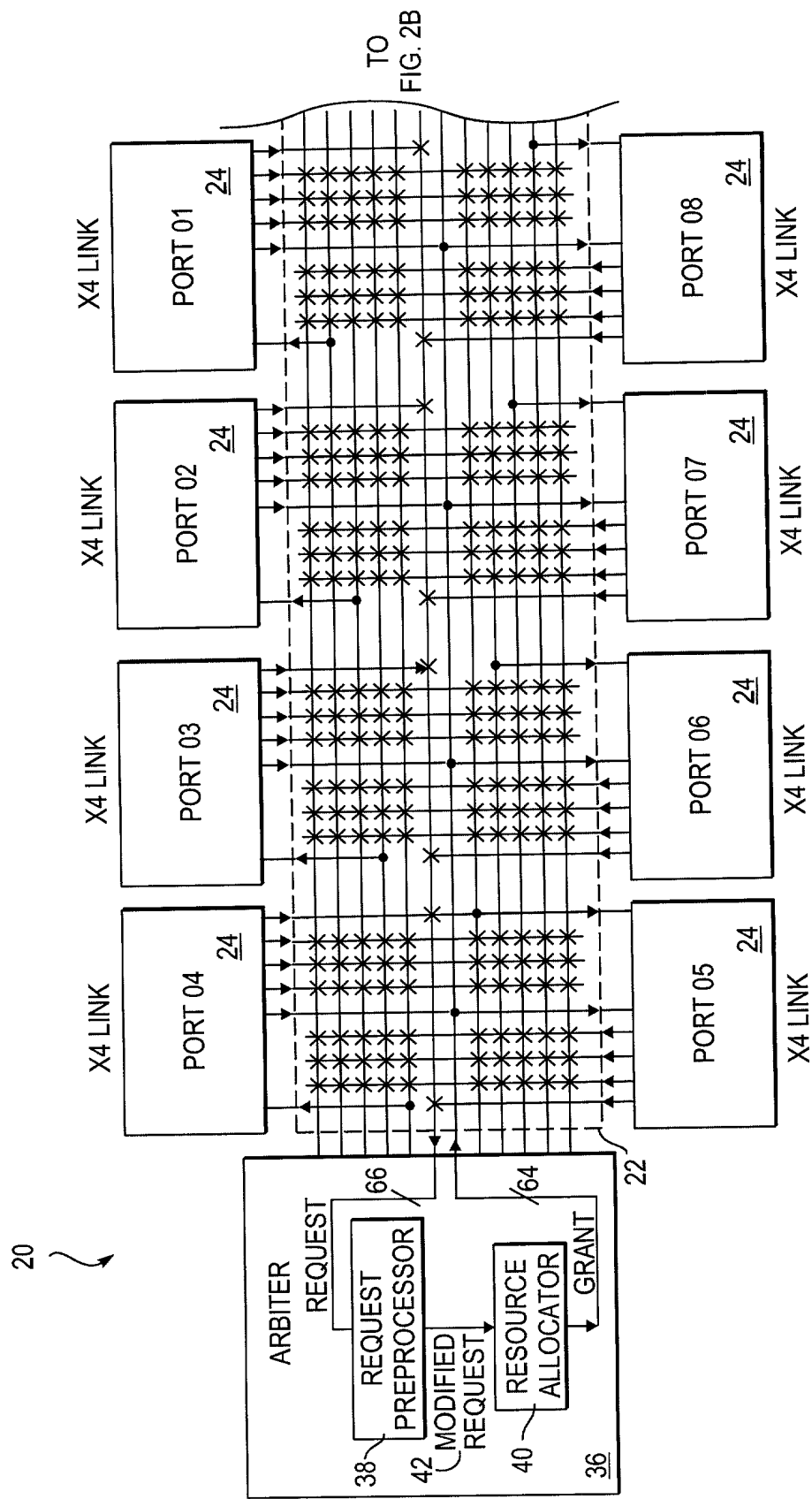


Fig. 2A

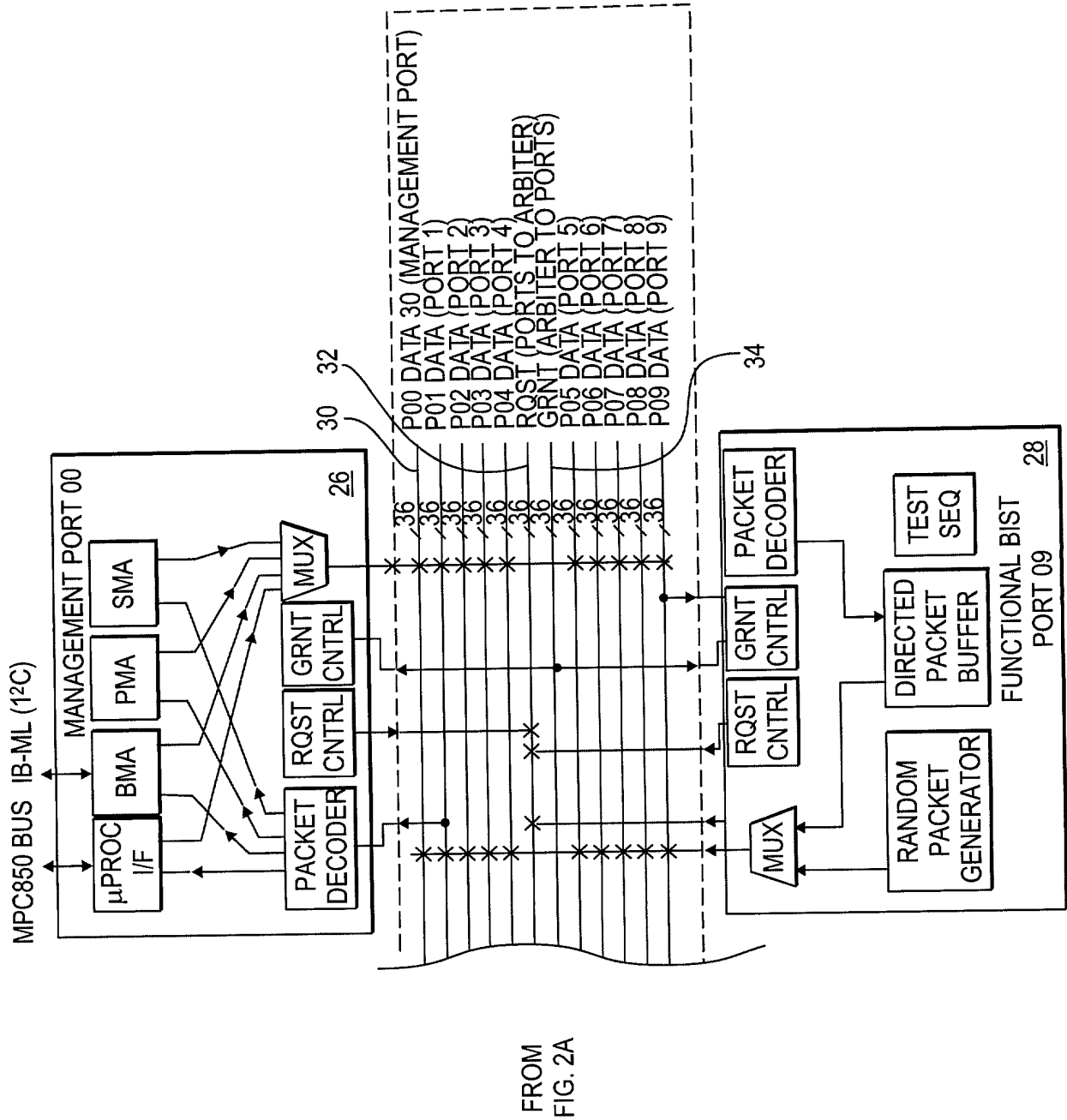


Fig. 2B

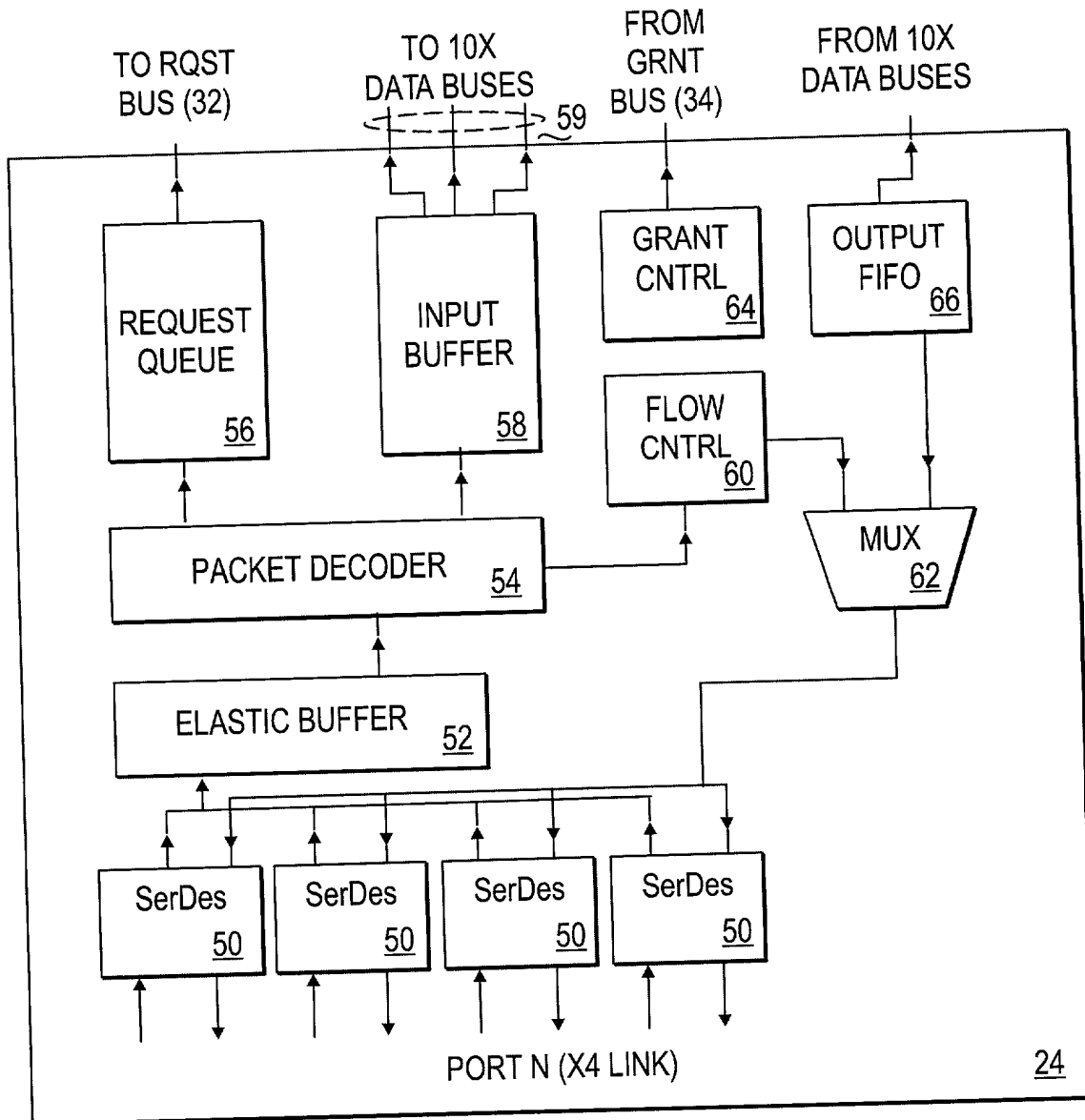


Fig. 3

DESTINATION
ROUTING
REQUEST

70

REQUEST EXTENSION (2 BIT) <u>96</u>	PARTITION KEY (16 BIT) <u>92</u>	DESTINATION ADDRESS (16 BIT) <u>90</u>	SERVICE LEVEL (4 BIT) <u>94</u>	PACKET LENGTH (11 BIT) <u>86</u>	REQUEST ID (10 BIT) <u>84</u>	INPUT PORT (5 BIT) <u>82</u>	REQUEST CODE (2 BIT) <u>80</u>
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DIRECT
ROUTING
REQUEST

72

REQUEST EXTENSION (2 BIT) <u>76</u>	RESERVED (27 BIT)	OUTPUT PORT (5 BIT) <u>88</u>	VIRTUAL LANE (4 BIT) <u>102</u>	PACKET LENGTH (11 BIT) <u>86</u>	REQUEST ID (10 BIT) <u>84</u>	INPUT PORT (5 BIT) <u>82</u>	REQUEST CODE (2 BIT) <u>80</u>
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CREDIT
UPDATE
REQUEST

74

PORT STATUS (2BIT) <u>98</u>	RESERVED (27 BIT)	OUTPUT PORT (5 BIT) <u>88</u>	VIRTUAL LANE (4 BIT) <u>102</u>	FLOW CONTROL CREDIT LIMIT (FCCL) (12 BIT) <u>104</u>	RESERVED (9 BIT)	INPUT PORT (5 BIT) <u>82</u>	REQUEST CODE (2 BIT) <u>80</u>
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Fig. 4

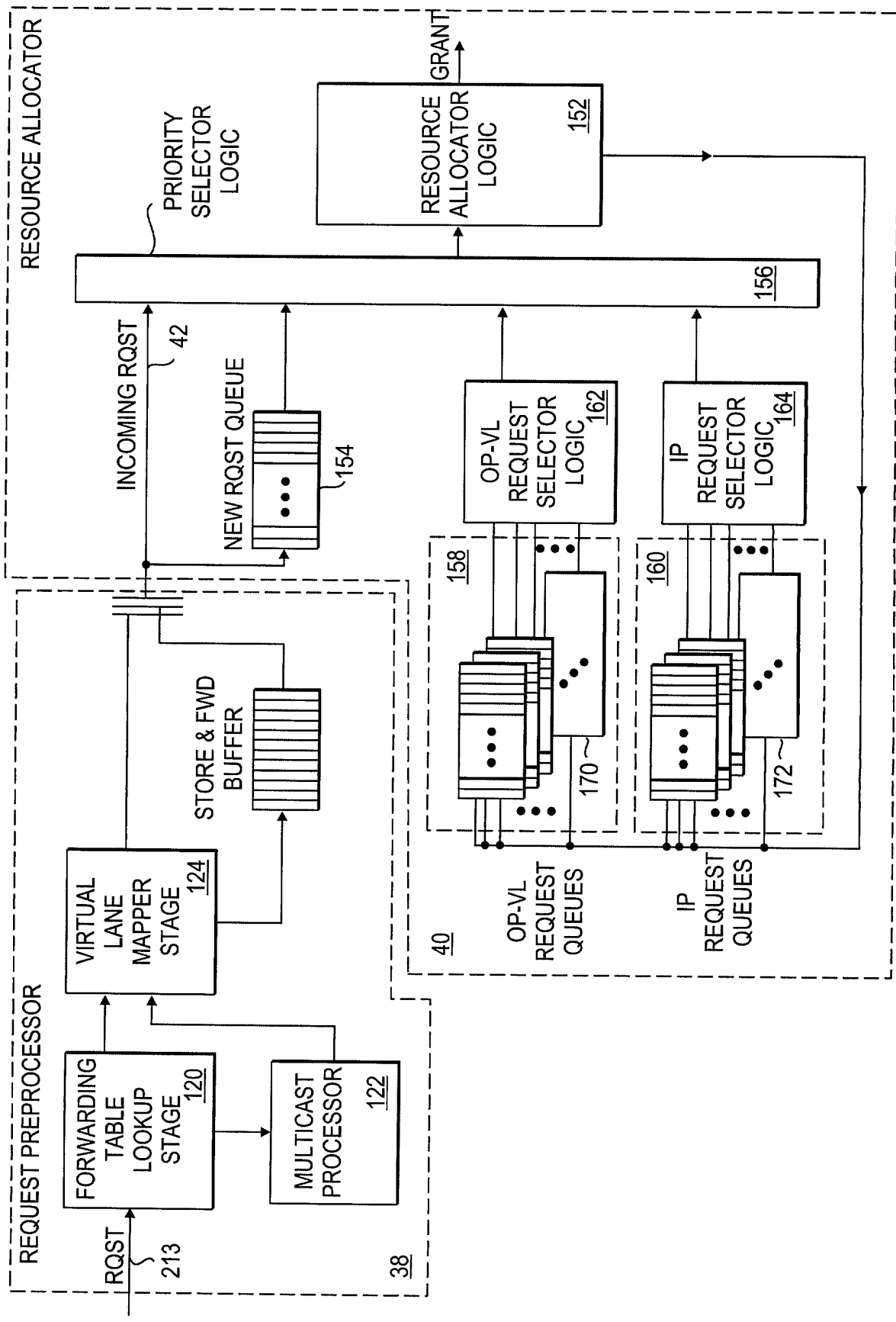


Fig. 5

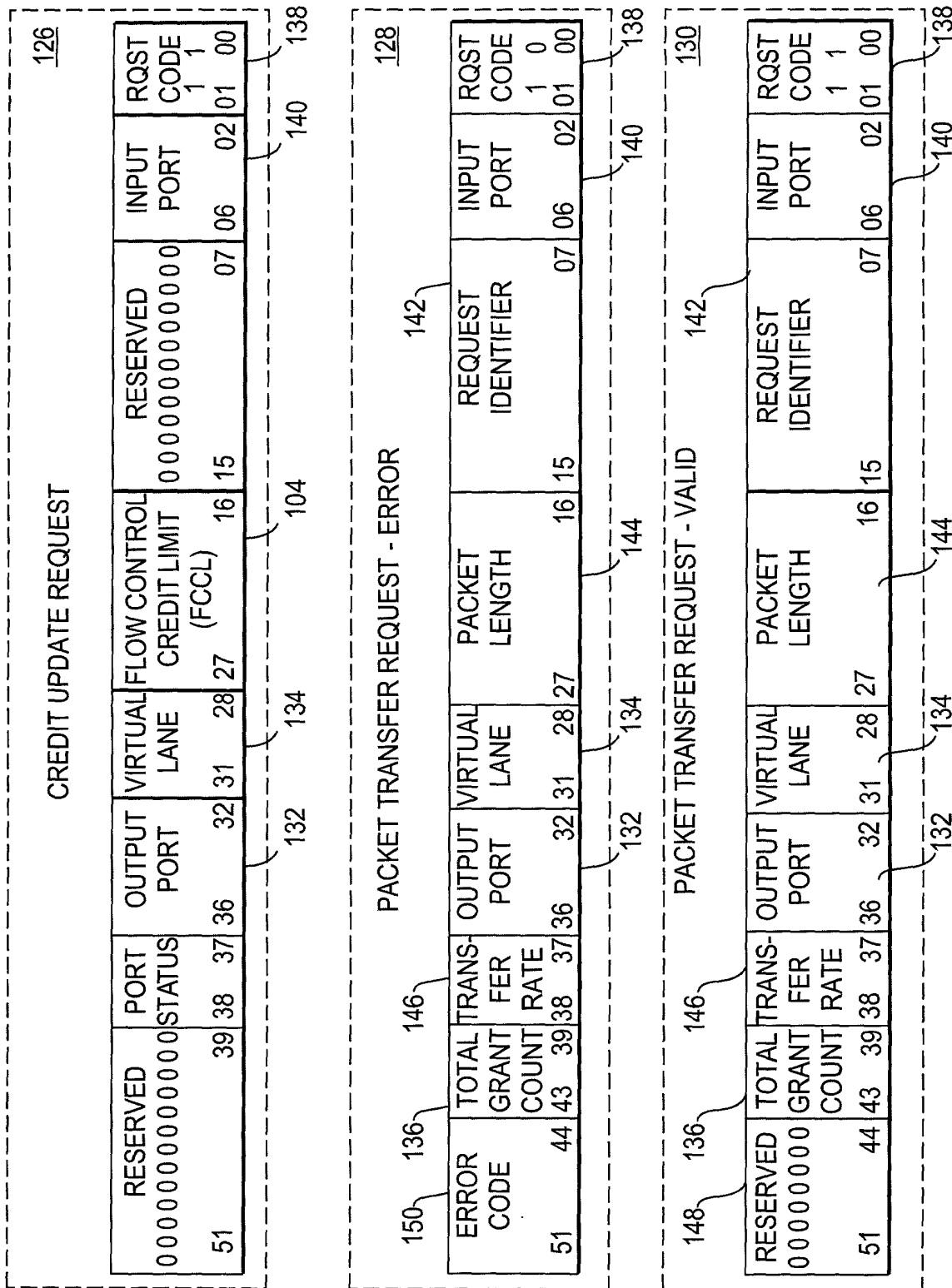


Fig. 6

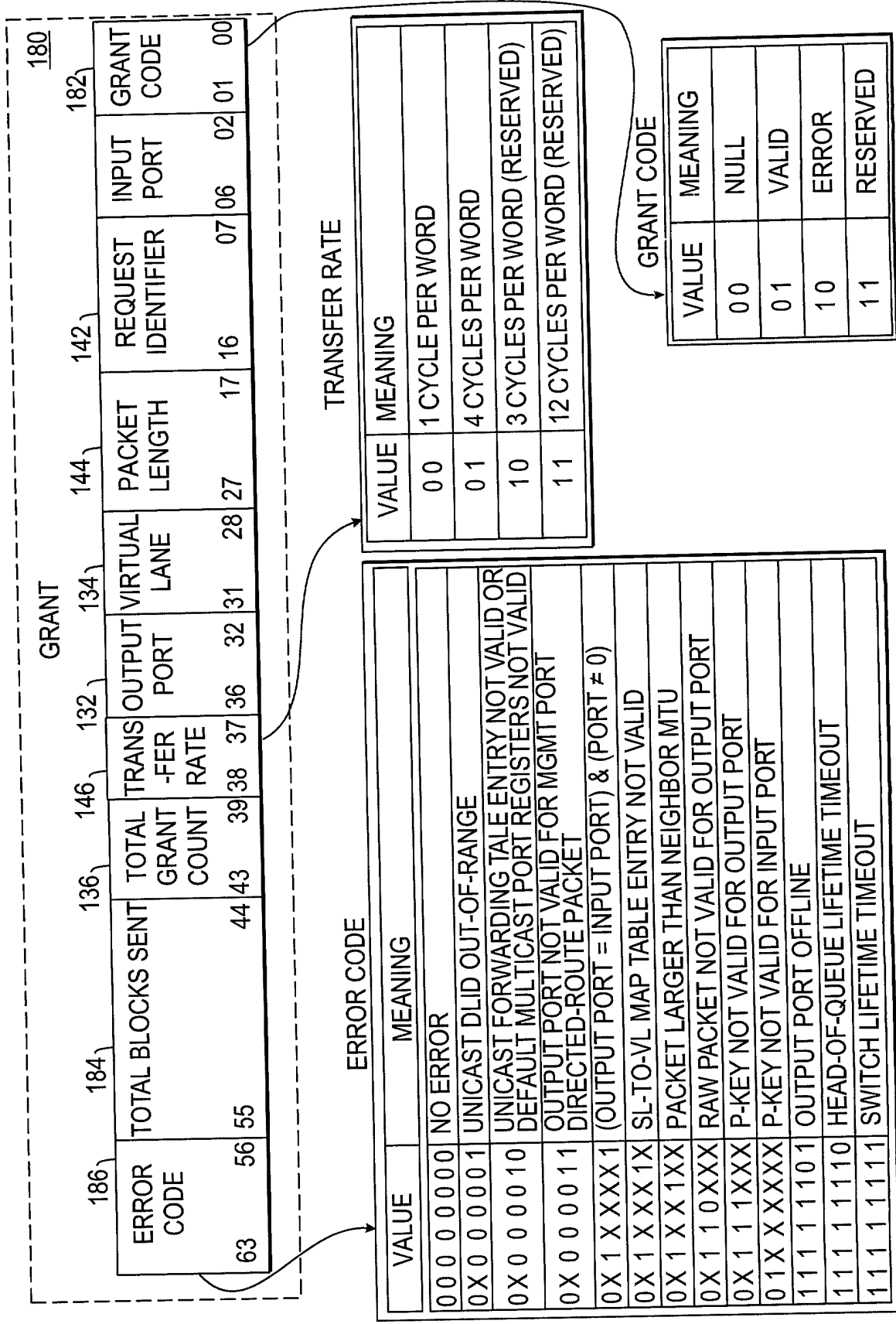


Fig. 7

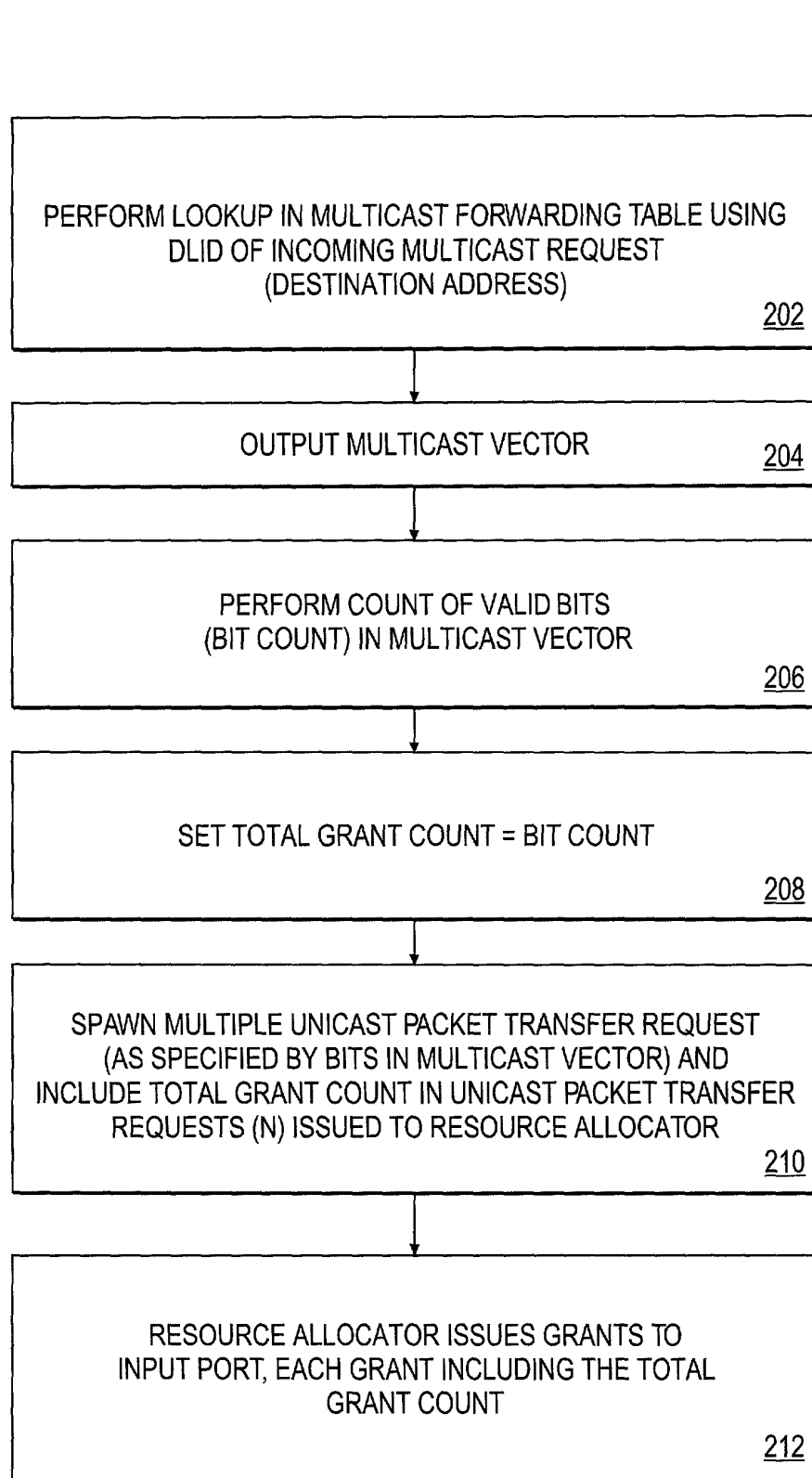


Fig. 8

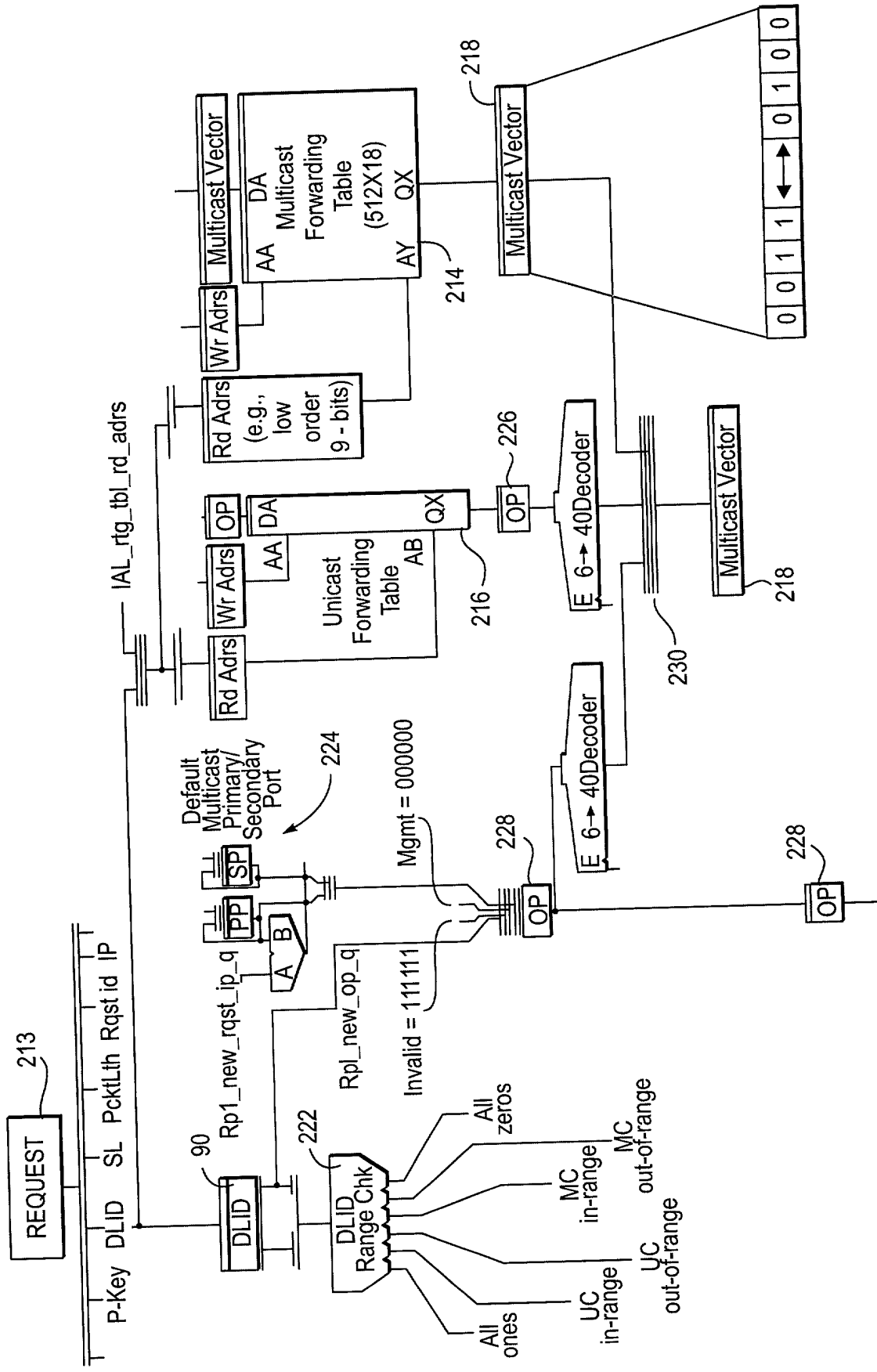


Fig. 9

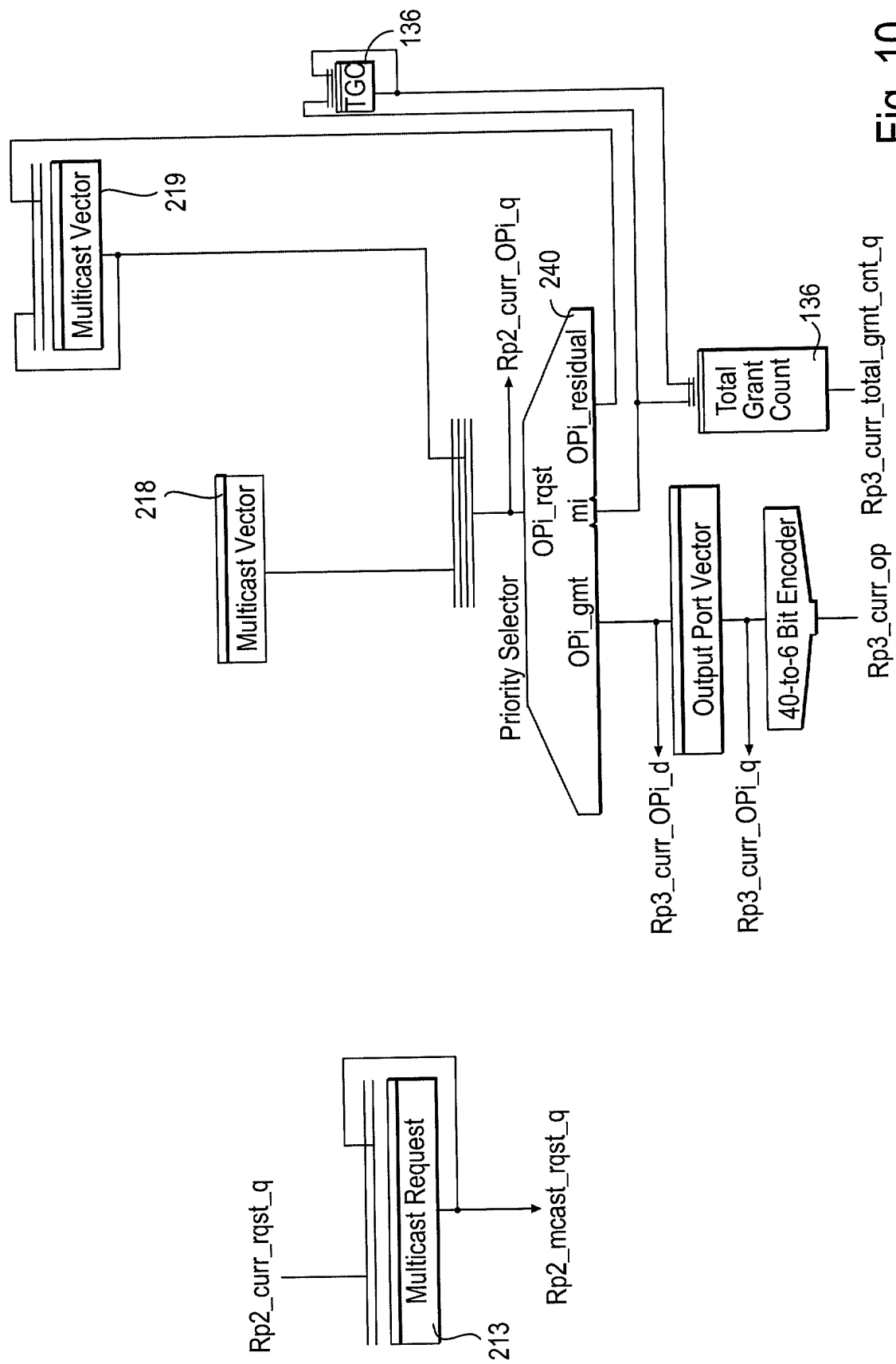


Fig. 10

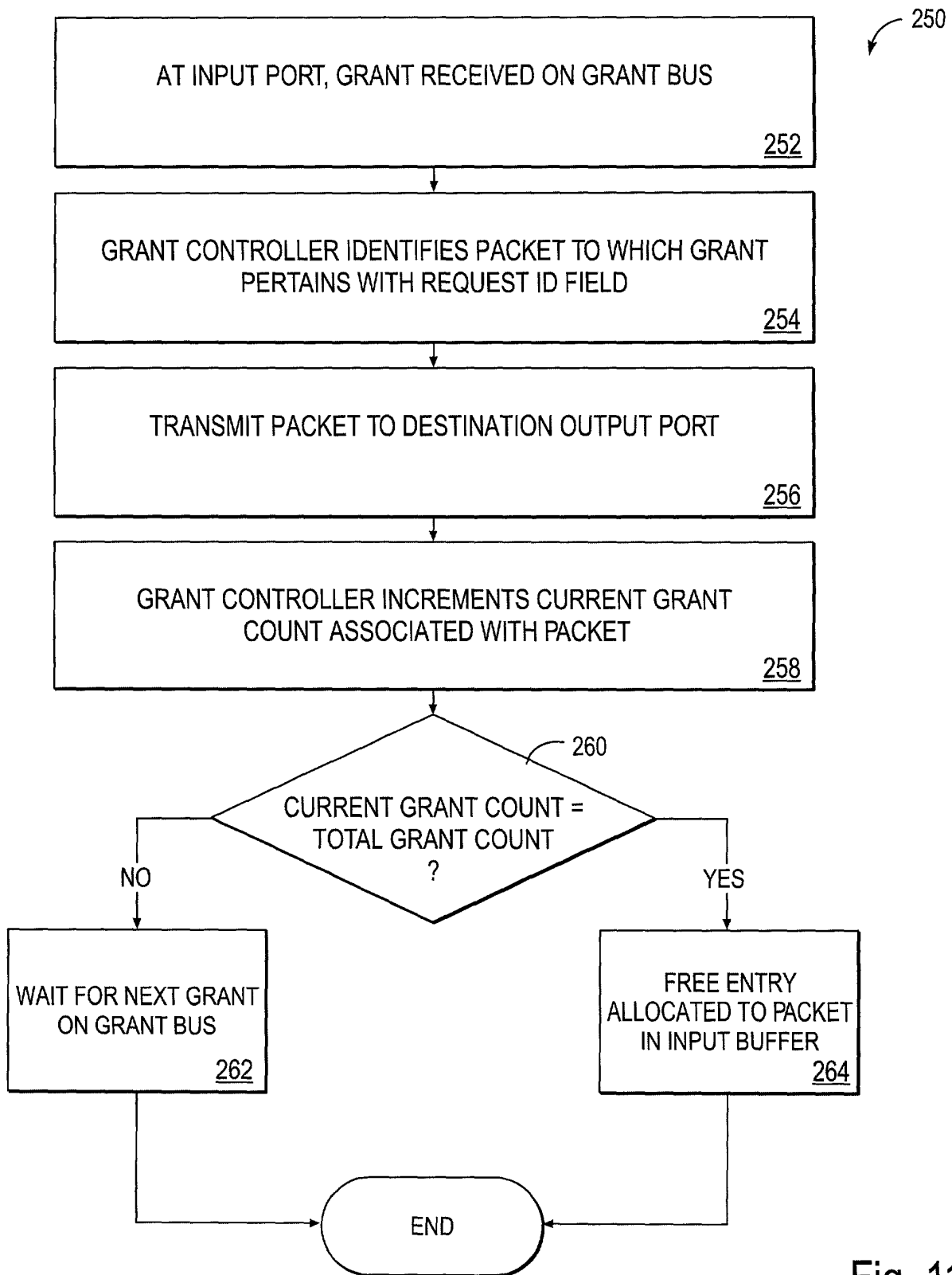


Fig. 11

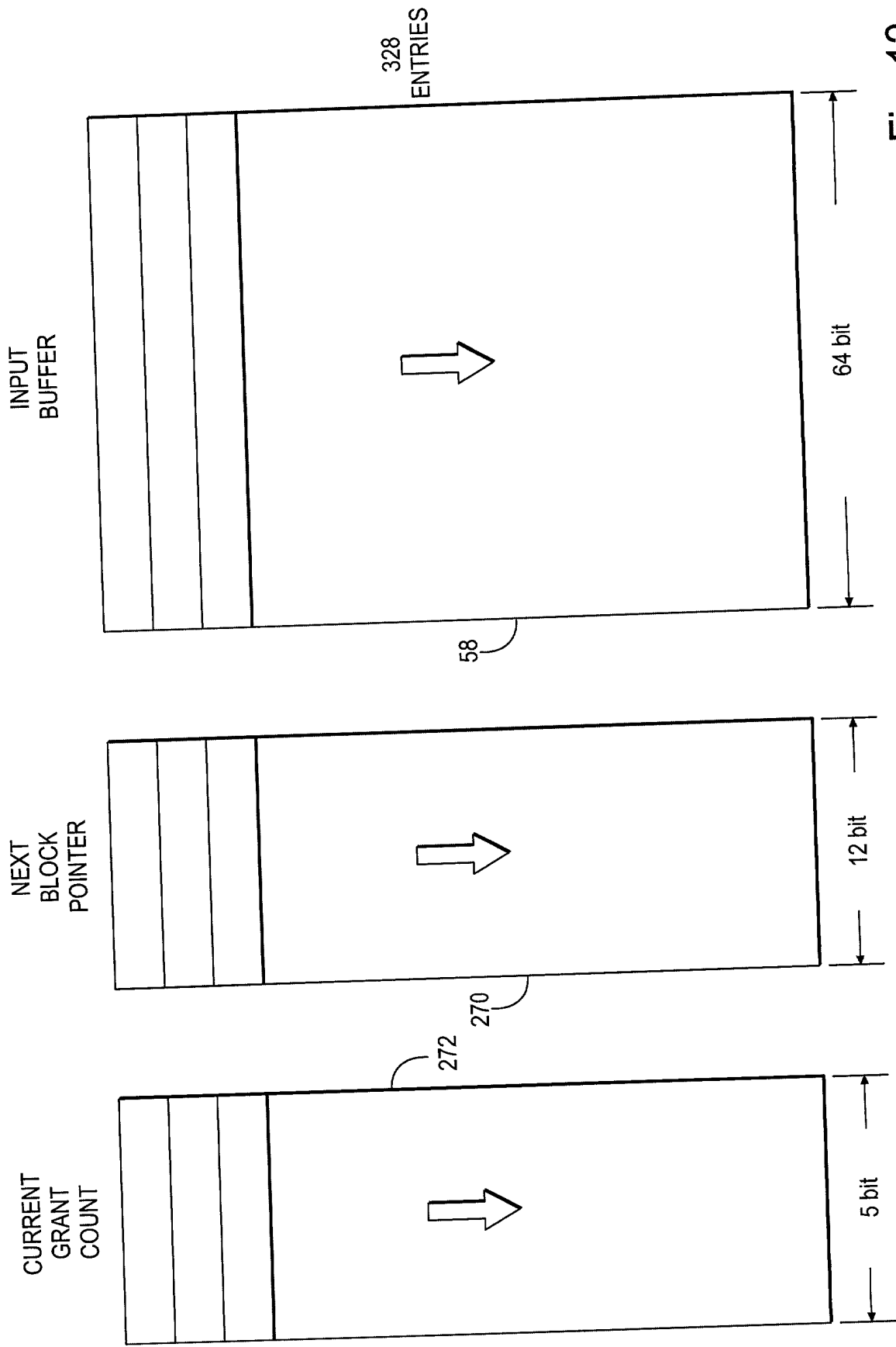


Fig. 12